| L Number | Hits | Search Text | DB | Time stamp |
|---|---|---|---|---|
| 1 | 1 | 714/41 and memory near3 (model$3 simulat$3 emulat$3) and (generat$3 forc$3 creat$3 provid$3) near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:16 |
| 2 | 1 | 714/33 and memory near3 (model$3 simulat$3 emulat$3) and (generat$3 forc$3 creat$3 provid$3) near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:14 |
| 3 | 3 | 714/33 and (generat$3 forc$3 creat$3 provid$3) near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:14 |
| 4 | 3 | 714/41 and (generat$3 forc$3 creat$3 provid$3) near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:15 |
| 7 | 2 | 714/$ and memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:18 |
| 6 | 1 | 714/33 and memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:17 |
| 5 | 1 | 714/41 and memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:17 |
| 8 | 1 | 703/$ and memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:19 |
| 9 | 0 | 711/$ and memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:19 |
| 10 | 2 | memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:19 |

| 11 | 2 | 714/$ and memory near3 (model$3 simulat$3 emulat$3) same inject$4 near2 error and error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:20 |
|---|---|---|---|---|
| 12 | 0 | 711/$ and memory near3 (model$3 simulat$3 emulat$3) same inject$4 near2 error and error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:20 |
| 13 | 1 | 703/$ and memory near3 (model$3 simulat$3 emulat$3) same inject$4 near2 error and error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:20 |
| 14 | 2 | memory near3 (model$3 simulat$3 emulat$3) same inject$4 near2 error and error same (address location) same memory same bit and (@ad<20000911 @rlad<20000911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 11:52 |
| 15 | 4 | memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error and error same (address location) same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:34 |
| 16 | 23 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near2 error and error same (address location) same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:28 |
| 17 | 15 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same (address location) same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:06 |
| 18 | 14 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:24 |
| 19 | 13 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (invert$4 flip$4 revers$4) and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:16 |
| 20 | 13 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (inver$4 flip$4 revers$4) and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:18 |

| 22 | | 11 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (lsi large) and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:17 |
|----|---|-----|---|---|---|
| 23 | | 11 | (memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (inver$4 flip$4 revers$4) and (@ad<19990911 @rlad<19990911)) and (memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit and (lsi large) and (@ad<19990911 @rlad<19990911)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:17 |
| 24 | | 2 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error same (inver$4 flip$4 revers$4) and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:20 |
| 25 | | 10 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit same (inver$4 flip$4 revers$4) and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:22 |
| 26 | | 7 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error and error same address same memory same bit same (inver$4 revers$4) and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:23 |
| 27 | | 2 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near error same (inver$4 revers$4) and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:23 |
| 28 | | 0 | memory near3 (model$3 simulat$3 emulat$3) same (large lsi) and (inject$4 forc$3) near error and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:24 |
| 29 | | 15 | memorysame (model$3 simulat$3 emulat$3) same (large lsi) and (inject$4 forc$3) near error and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:25 |
| 30 | | 0 | memory with (model$3 simulat$3 emulat$3) with (large lsi) and (inject$4 forc$3) near error and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:27 |
| 31 | | 0 | memory same (model$3 simulat$3 emulat$3) same (large near scale lsi) and (inject$4 forc$3) near error and error same address same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:27 |

| 32 | 0 | memory same (model$3 simulat$3 emulat$3) same (large near scale lsi) and (inject$4 forc$3) near error and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:27 |
|----|---|---|---|---|
| 33 | 6 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near2 error and error same (address location) same memory same bit same size and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:29 |
| 34 | 3 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near2 error and error same (address location) same memory same bit and (read write error) near3 size and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:31 |
| 35 | 3 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near2 error and error same (address location) same memory same bit and (read write error) near2 size and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:31 |
| 36 | 0 | memory near3 (model$3 simulat$3 emulat$3) and (inject$4 forc$3) near2 error and error same (address location) same memory same bit and (read write error) near size and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:31 |
| 37 | 1 | memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error with (read write) and error same (address location) same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:34 |
| 38 | 2 | memory near3 (model$3 simulat$3 emulat$3) and inject$4 near2 error with (read$3 writ$3) and error same (address location) same memory same bit and (@ad<19990911 @rlad<19990911) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/27 12:35 |